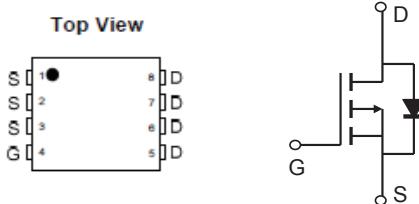
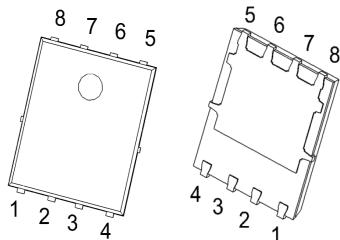


■ Features

- V_{DS} -60 V
- I_D (at $V_{GS}=-10V$) -80 A
- $R_{DS(ON),TYP}$ (at $V_{GS} = - 10V$) = 7.2 mΩ
- $R_{DS(ON), TYP}$ (at $V_{GS} = - 4.5V$) = 8.6 mΩ
- P-Channel, -5V Logic Level Control
- Very low on-resistance $R_{DS(on)}$
- Fast Switching
- Enhancement mode
- 100% Avalanche Tested

PDFN5x6-8

**■ Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted.)**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	-80	A
		-51	
Pulsed Drain Current (Note 1)	I_{DM}	-300	
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	56	mJ
Power Dissipation	P_D	115	W
Thermal Resistance, Junction- to-Ambient	$R_{\theta JA}$	48	°C/W
Thermal Resistance, Junction- to-Case	$R_{\theta JC}$	1.3	
Operating Junction and Storage Temperature	T_J, T_{stg}	-55 to 175	°C

Notes:

1. Repetitive rating; pulse width limited by max. junction temperature.
2. Limited by T_{Jmax} , starting $T_J = 25^\circ C$, $L = 0.5mH$, $R_G = 25\Omega$, $I_{AS} = -15A$, $V_{GS} = -10V$.

■ Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{I}_D = -250\mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	-60			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}} = -60\text{V}, \text{V}_{\text{GS}} = 0\text{V}$			-1	μA
		$\text{V}_{\text{DS}} = -60\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{T}_J = 125^\circ\text{C}$			-100	
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = \pm 20\text{V}$			± 100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = -250\mu\text{A}$	-1	-1.7	-2.5	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}} = -10\text{V}, \text{I}_D = -20\text{A}$		7.2	9.4	$\text{m}\Omega$
		$\text{V}_{\text{GS}} = -4.5\text{V}, \text{I}_D = -10\text{A}$		8.6	11.4	
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = -30\text{V}, f = 1\text{MHz}$		6985		pF
Output Capacitance	C_{oss}			450		
Reverse Transfer Capacitance	C_{rss}			290		
Gate Resistance	R_g	$f = 1\text{MHz}$		13.8		Ω
Total Gate Charge	Q_g	$\text{V}_{\text{DS}} = -30\text{V}, \text{I}_D = -20\text{A}, \text{V}_{\text{GS}} = -10\text{V}$		94		nC
Gate Source Charge	Q_{gs}			21		
Gate Drain Charge	Q_{gd}			25		
SWITCHING CHARACTERISTICS (Note 4)						
Turn-On DelayTime	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{DD}} = -30\text{V}, \text{I}_D = -5\text{A}, \text{R}_g = 6.8\Omega, \text{V}_{\text{GS}} = -10\text{V}$		19		ns
Turn-On Rise Time	t_r			26		
Turn-Off DelayTime	$\text{t}_{\text{d(off)}}$			89		
Turn-Off Fall Time	t_f			45		
DRAIN-SOURCE DIODE CHARACTERISTICS						
Reverse Recovery Time	t_{rr}	$\text{I}_{\text{SD}} = -20\text{A}, \text{V}_{\text{GS}} = 0\text{V}, \frac{\text{dI}}{\text{dt}} = -500\text{A}/\mu\text{s}$		35		ns
Reverse Recovery Charge	Q_{rr}			175		
Diode Forward Voltage	V_{SD}	$\text{I}_{\text{SD}} = -35\text{A}, \text{V}_{\text{GS}} = 0\text{V}$		-0.88	-1.3	V

Notes:

3. Pulse Test: pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
4. Switching characteristics are independent of operating junction temperatures.

■ Typical Characteristics

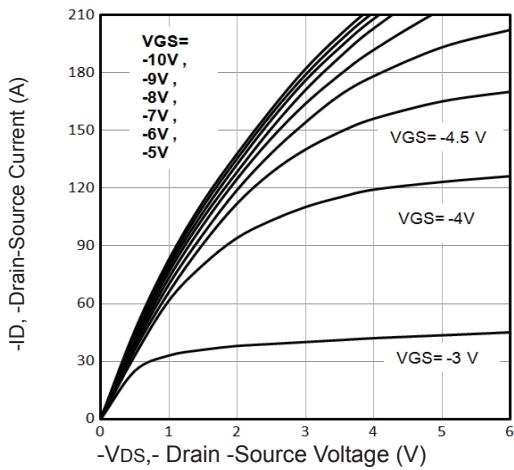


Fig1. Typical Output Characteristics

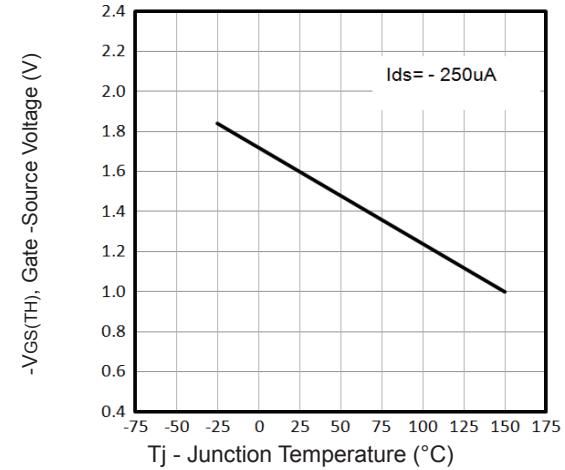


Fig2. Threshold Voltage Vs. Temperature

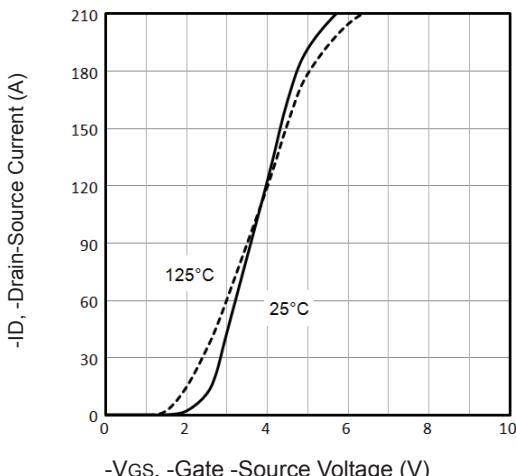


Fig3. Typical Transfer Characteristics

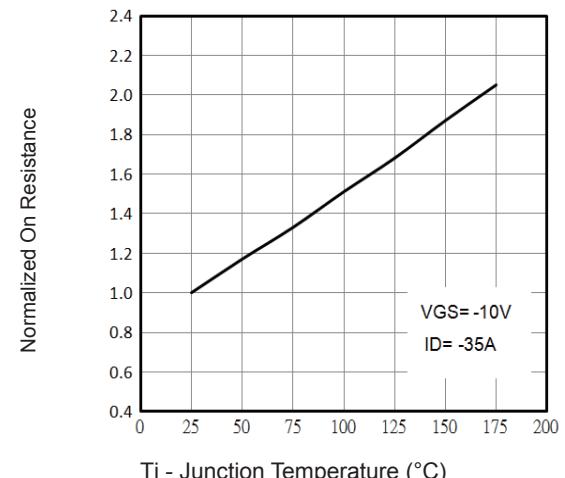


Fig4. Normalized On-Resistance Vs. Temperature

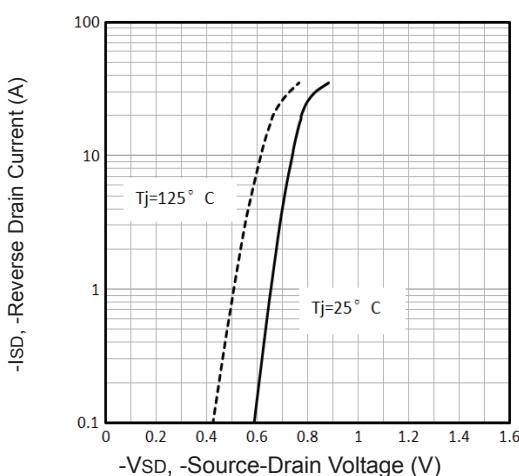


Fig5. Typical Source-Drain Diode Forward Voltage

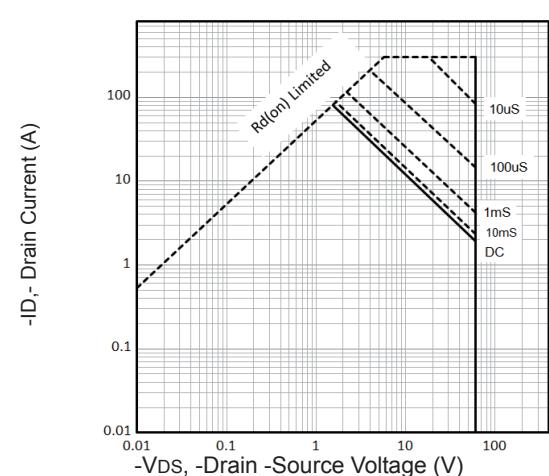


Fig6. Maximum Safe Operating Area

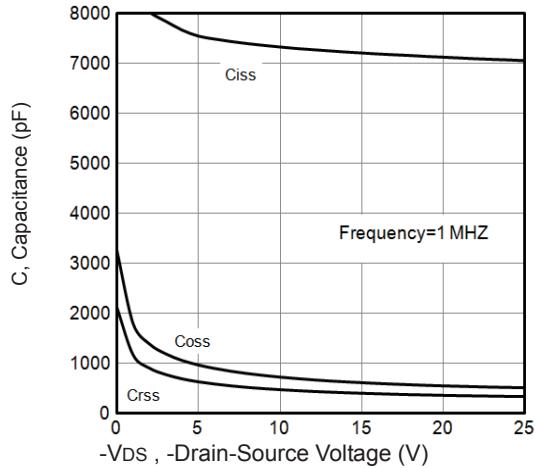


Fig7. Typical Capacitance Vs.Drain-Source Voltage

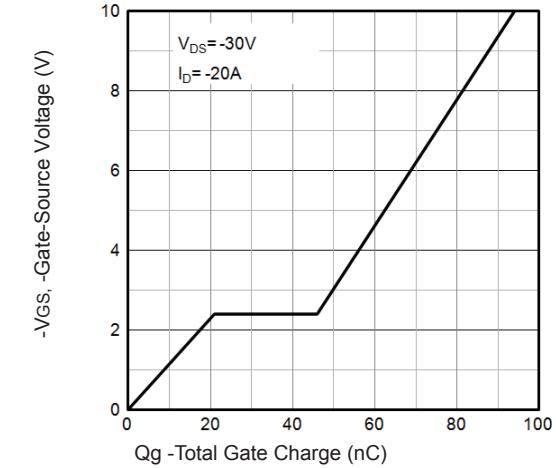


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

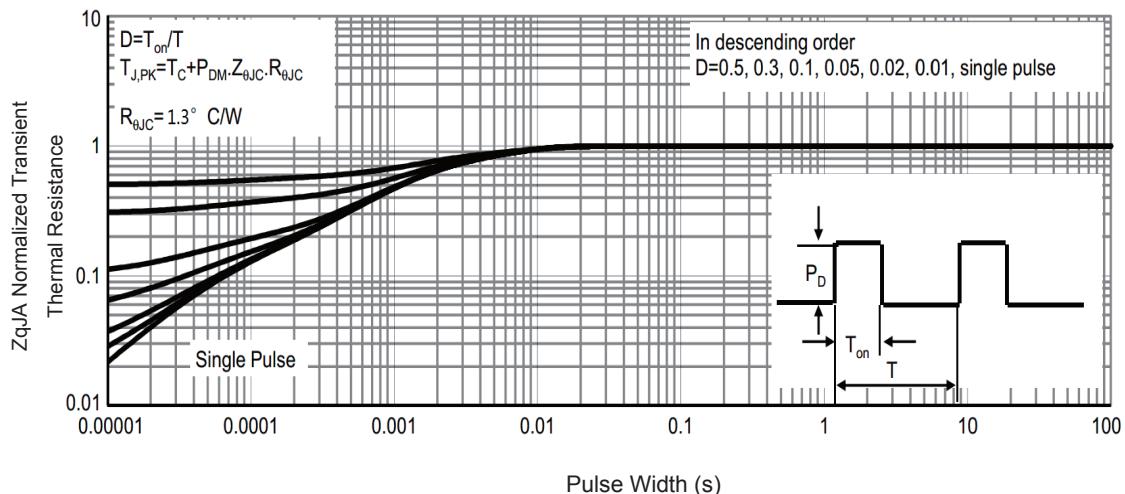


Fig9. Normalized Maximum Transient Thermal Impedance T_j -Junction

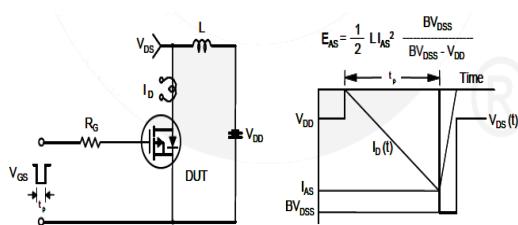


Fig10. Unclamped Inductive Test Circuit and Waveforms

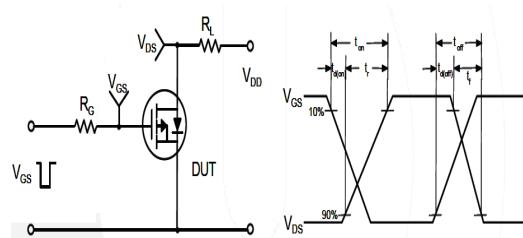
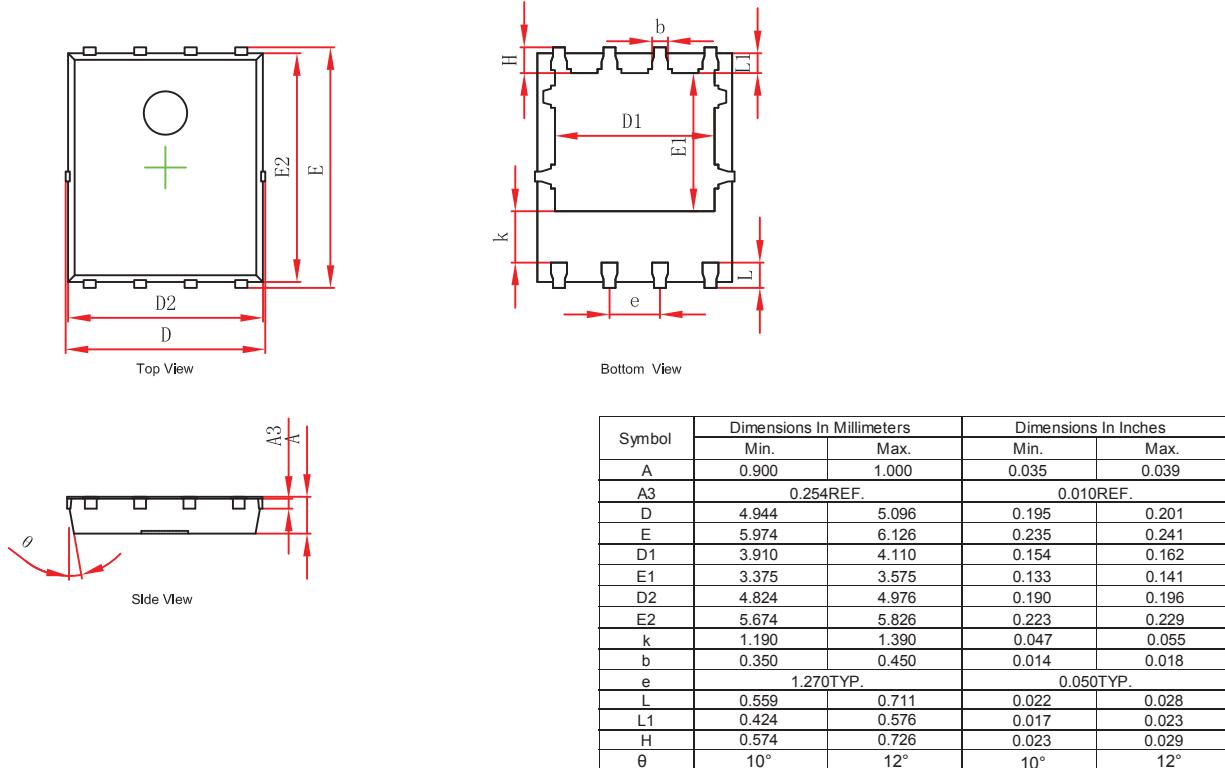
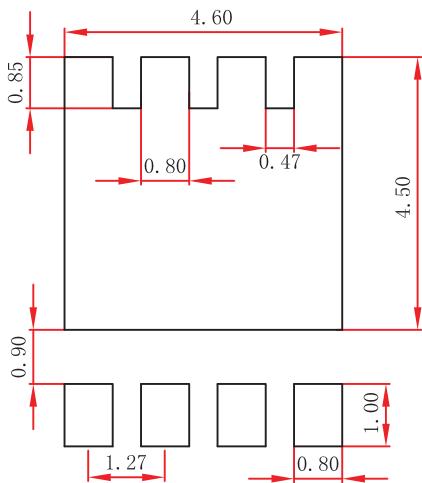


Fig11. Switching Time Test Circuit and waveforms

■ PDFN5x6-8 Package Outline Dimensions



■ PDFN5x6-8 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.